Dimmentionement et évaluation des architectures

Bilel CHERIF
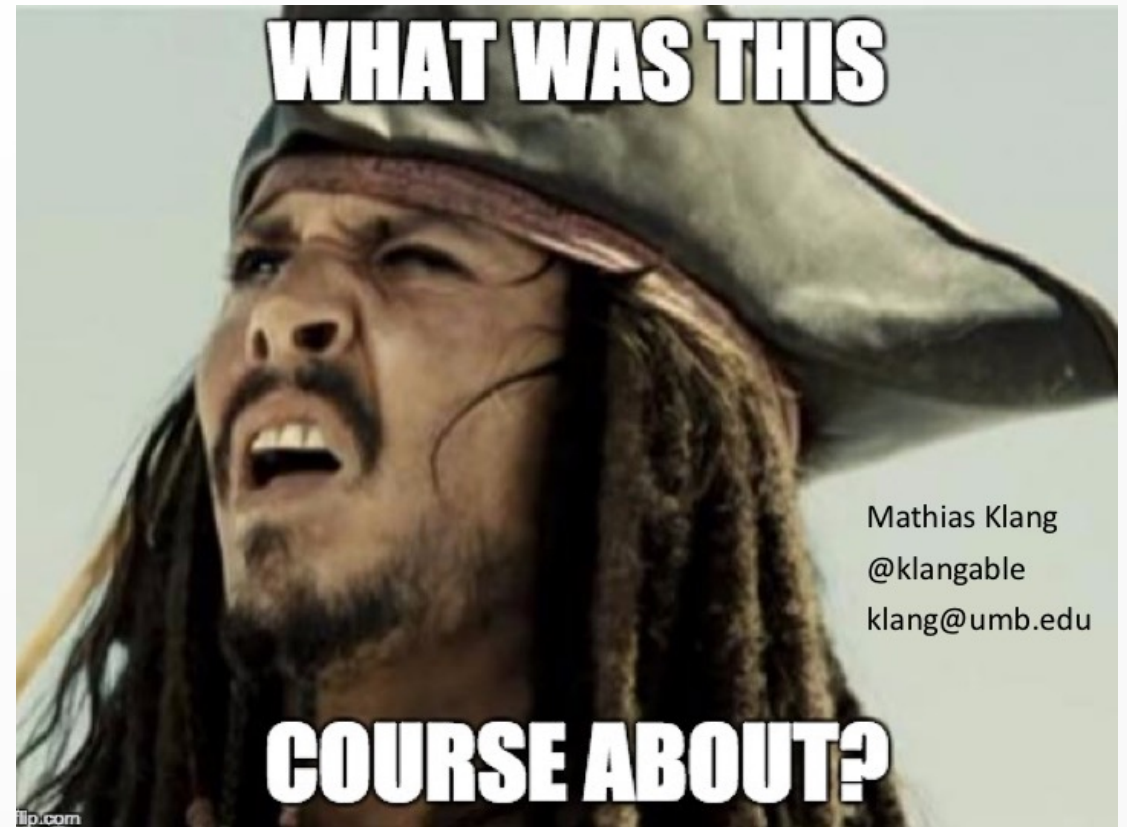**5A-SIEC**

Courses

Course 0x01:

- Introduction.
- Co-design.
- Zynq architecture overview.

Course 0x02:

- More on zynq peripherals
- Zynq co-processors.

Course 0x03:

- PS-PL interfacing.
- AXI interface.

WHAT WAS THIS COURSE ABOUT?

Mathias Klang
@klangable
klang@umb.edu

TD

TD 0x01:

- Hands on zybo board and vivado tool.

TD 0x02:

- PS-PL interfaces handshaking.

TD 0x03:

- Hardware debugging tools.

TP

TP0x01:

- Hands on vivado SDK tools.
- PS exploration.
- Peripherals configuration and usage.

TP0x02:

- PL exploration.
- AXI interface implementation.
- PS-PL interfacing.

Project.

Guitar multi-effects project.

- Simples acquisition through the dedicated audio interface.
- Developing audio effects on fpga.
- Control software implementation on ps.

"Let the bodies hit the ...

FLOOOOOOOOOOOOOOR!!!"

Course web page:
https://xobx-cherif.github.io/zynq_be/